

The future of intelligent microcontrollers.











Everything you need on a single chip

Alif Semiconductor provides scalable, secure, low-power 32-bit microcontrollers and fusion processors with integrated AI-accelerators and wireless connectivity. The foundational Ensemble® and Balletto™ product families redefine what is achievable in terms of processing, machine learning capability, and runtime for battery-operated endpoint devices.

The Ensemble family

Ensemble delivers high-performance, low-power, accelerated on-device AI/ML, imaging & graphics processing, and precise real-time control.

The Ensemble family scales from single and dual core MCUs combining up to two Cortex-M55 MCU

cores, on to triple and quad core fusion processors that add up to two Cortex-A32 microprocessor cores capable of running high-level operating systems. Ensemble devices also scale up to include one or two Ethos-U55 microNPUs to accelerate machine learning workloads.

The Balletto family

Balletto is the world's first microcontroller that combines wireless connectivity and hardware accelerated AI/ML processing. Based on the Ensemble E1 series, Balletto extends its capabilities by adding a fully integrated radio subsystem that supports Bluetooth Low

Energy 5.3 and 802.15.4 wireless connectivity. The powerful Cortex-M55 CPU plus the Ethos-U55 neural processor makes Balletto an ideal single-chip solution for wearables, hearables, and low-power IoT applications.

PRODUCT TABLE																													
Feature	Available Memory Processors			High Speed Connectivity	Serial Connectivity				Display	Graphics Camera		1	Audio		Memory Expansion Timers					Analog	Analog						Operation Rang	nge Package	
	MRAM SRAM Cortex-A32	Cortex-M55	Ethos-U55 NPU	USB 2.0 Ethernet SDIO			MIPI-I3C	CAN-FD	Parallel		GPU Paralle			I2S PDM 8		SD v4.2/	32-bit Univ- 32	2-bit Low Q	Quadrature F	TC WDT		12-bit ADC	24-bit ΣΔ ADC	12-bit DAC	High-Speed Comparator	Low-Power		v Supply Voltage	е Туре
				HS/FS 10/100	up to 2.5 Mbps up	to 50 Mbps up to 3.4 Mb	pps	up to 10 Mbps	s	2-Lane		Parallel	2-Lane	mic inp	uts	eMMC v5.1	ersal Timer Po	ower Timer E	Encoder		Sensor				Comparator	Comparator	to 3.3v		
The Ensemble Fa	ımily																												
E7 Series	TEEND LOCKED TOWARD WITE	4:4/0 \ : 4: 400 \	to 4/ 0000 a to 004 0000	Ϋ	Y 0 (4 20 405) 5			1.	Y 4 4	· Y.	9044	O.b.	T. Y		Y •	·	10				7.	0 10 :	4. 4 4/66	ta Ou Outumal	- 4. 4/ Samuela	die dieneste	100	17/4- 40/	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\
	5AS 5.5 MB 13.5 MB 2x 800 MHz				9 (4 w RS-485) 5	5		<u> </u>	1x up to 24-bit	$\overline{}$	8-16b 8-16b	_		5 2	2		12 4	4	+ !	4	<u> </u>		1x, 4 differential inpu		_			1.7V to 4.2V	WLCSP208 FBGA194
	5LS 5.5 MB 13.5 MB 2x 800 MHz	IX 160 MHZ + IX 400 MHZ	IX 46 GUPS + IX 204 GUPS		9 (4 w RS-485) 5	5			1x up to 24-bit	1	8-100	80		5 2	2	<u> </u>	12 4	4	+	4		3x, 18 inputs	1x, 4 differential inpu	ts 2x, 2 channels	s 4x, 16 inputs	ix, 4 inputs	120 8	1.7V to 4.2V	FBGAI94
E5 Series AE512F80F55D5	AS 5.5 MB 13.5 MB 1x 800 MHz	1v 160 MUz ± 1v 400 MUz	1x 46 GOPS + 1x 204 GOPS		9 (4 w RS-485) 5	=	1	1	1x up to 24-bit	1 1	8-16b	0h	T 1	5 2	7 2	1	12 4		, ,	4	7,	2v 10 inpute	1x, 4 differential inpu	to 2v 2 shappel	a 4v 16 inputa	1v. 4 inputo	120 8	1.7V to 4.2V	WLCSP208
AE512F80F55D5	- 		1x 46 GOPS + 1x 204 GOPS	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	9 (4 w RS-485) 5	5	1	1	1x up to 24-bit	-	8-16b		'	5 2	2	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	12 4		; '	4	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\		1x, 4 differential inpu	_			120 8	1.7V to 4.2V	FBGA194
AE512F80F5582	\rightarrow		1x 46 GOPS + 1x 204 GOPS	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	9 (4 w RS-485) 5	5	1	1	1x up to 24-bit	-	8-16b		<u> </u>	5 2	2	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	12 4		1 1	4	 		1x, 4 differential inpu					1.7V to 4.2V	WLCSP208
AE512F80F5582			1x 46 GOPS + 1x 204 GOPS	\ 	9 (4 w RS-485) 5	5	1	1	1x up to 24-bit		8-16b 8-16b		 	5 2	2	<u>'</u>	12 4		1 1	4	 		1x, 4 differential inpu			<u></u>		1.7V to 4.2V	FBGA194
E3 Series	-EO T 0.3 MID T 0.23 MID T 1X 000 MINZ	1X 100 MHZ * 1X 400 MHZ	1X 40 00F3 + 1X 204 00F3		7 (4 W K3-465) 5			'	1x op to 24-bit	<u> </u>		00	<u> </u>				4	- 4		7	'	OX, 10 IIIputs	ix, 4 differentiar inpo	ZX, Z CHUIIITEIX	5 4x, 10 IIIpots	1x, 4 inputs	0	1.77 10 4.27	T DOAI94
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	2AE 5.5 MB 8.25 MB		1x 46 GOPS + 1x 204 GOPS	1 1 1	9 (4 w RS-485) 5	5	1	1	1x up to 24-bit	-	8-16b		1	5 2	2	1 1	12 4	4	4 1	2	1		1x, 4 differential inpu				120 8	1.7V to 4.2V	WLCSP208
D	2LE 5.5 MB 8.25 MB	+	1x 46 GOPS + 1x 204 GOPS	1 1 1	9 (4 w RS-485) 5	5	1	1	1x up to 24-bit	-	8-16b		1	5 2	2	1 1	12 4	4	4 1	2	1		1x, 4 differential inpu		· ·	 	120 8	1.7V to 4.2V	FBGA194
AE302F80C1557	LE 1.5 MB 5.75 MB	1x 160 MHz + 1x 400 MHz	1x 46 GOPS + 1x 204 GOPS	1 1 1	9 (4 w RS-485) 5	5	1		1x up to 24-bit	1 1	8-16b	8b	1	5 2	2	1 1	12 4	4	4 1	2	1	3x, 18 inputs	1x, 4 differential inpu	ts 2x, 2 channels	s 4x, 16 inputs	1x, 4 inputs	120 8	1.7V to 4.2V	FBGA194
AE302F40C1537	\longrightarrow	1x 160 MHz + 1x 400 MHz	1x 46 GOPS	1 1 1	9 (4 w RS-485) 5	5	1		1x up to 24-bit		8-16b		1	5 2	2	1 1	12 4	4	4 1	2	1		1x, 4 differential inpu			<u></u>	120 8	1.7V to 4.2V	FBGA194
E1 Series																							1				<u> </u>		
AE101F4071542LI	H 1.5 MB 4.5 MB	1x 160 MHz	1x 46 GOPS	1 1	7 (2 w RS-485) 5	5	1	1	1x up to 24-bit	1 1	Ĭ	8b	Ī	3 2	1	1	8 2	4	4 1	1	1	2x, 12 inputs	1x, 4 differential inpu	ts 1x, 1 channel	2x, 8 inputs	1x, 4 inputs	120 8	1.7V to 4.2V	FBGA194
E1C Series				-										,				<u>'</u>										_	
AE1C1F4051920P	PH 1.9 MB 2.0 MB	1x 160 MHz	1x 46 GOPS	1 1	7 (2 w RS-485) 4	3	1	2	1x up to 24-bit	1 1	Ĭ	8b		3 1	1	1 4	4 2	4	4 1	1	1	2x, 12 inputs	1x, 4 differential inpu	ts 1x, 1 channel	2x, 8 inputs	1x, 4 inputs	71 6	1.7V to 4.5V	FBGA120
AE1C1F4051920H	H 1.9 MB 2.0 MB	1x 160 MHz	1x 46 GOPS	1 1	7 (2 w RS-485) 4	3	1	2	Ĭ	1 1	Ĭ	8b	I	3 1	1	1 4	4 2	4	4 1	1	1	2x, 12 inputs	1x, 4 differential inpu	ts 1x, 1 channel	2x, 8 inputs	1x, 4 inputs	36 6	1.7V to 4.5V	WLCSP90
AE1C1F40319205	5H 1.9 MB 2.0 MB	1x 160 MHz	1x 46 GOPS	1 1	7 (2 w RS-485) 4	3	1	2	Ĭ		Ĭ	8b	I	3 1	1	1 4	4 2	4	4 1	1	1	2x, 12 inputs	1x, 4 differential inpu	ts 1x, 1 channel	2x, 8 inputs	1x, 4 inputs	36 6	1.7V to 4.5V	TQFP64
AE1C1F1041010PH	1.0 MB 1.0 MB	1x 160 MHz		1 1	7 (2 w RS-485) 4	3	1		Ĭ		<u> </u>	8b		3 1	1	1 4	4 2	4	4 1	1	1	2x, 12 inputs	1x, 4 differential inpu	ts 1x, 1 channel	2x, 8 inputs	1x, 4 inputs	71 6	1.7V to 4.5V	FBGA120
AE1C1F1041010HH	1.0 MB 1.0 MB	1x 160 MHz		1 1	7 (2 w RS-485) 4	3	1		Ĭ	Ĭ	Ĭ	8b	Ĭ	3 1	1	1 4	4 2	4	4 1	1	1	2x, 12 inputs	1x, 4 differential inpu	ts 1x, 1 channel	2x, 8 inputs	1x, 4 inputs	36 6	1.7V to 4.5V	WLCSP90
AE1C1F10410105H	1.0 MB 1.0 MB	1x 160 MHz		1 1	7 (2 w RS-485) 4	3	1		Ĭ		Ĭ	8b	Ĭ	3 1	1	1 4	4 2	4	4 1	1	1	2x, 12 inputs	1x, 4 differential inpu	ts 1x, 1 channel	2x, 8 inputs	1x, 4 inputs	36 6	1.7V to 4.5V	TQFP64
AE1C1F1040505P	PH 0.5 MB 0.5 MB	1x 160 MHz		1	7 (2 w RS-485) 4	3	1		Ĭ	<u> </u>	Ĭ	8b	Ĭ	3 1	1	1 4	4 2	4	4 1	1	1	2x, 12 inputs	1x, 4 differential inpu	ts 1x, 1 channel	2x, 8 inputs	1x, 4 inputs	71 6	1.7V to 4.5V	FBGA120
AE1C1F1040505H	HH	1x 160 MHz		1 1	7 (2 w RS-485) 4	3	1		Ĭ			8b	<u> </u>	3 1	1	1 4	4 2	4	4 1	1	1	2x, 12 inputs	1x, 4 differential inpu	ts 1x, 1 channel	2x, 8 inputs	1x, 4 inputs	36 6	1.7V to 4.5V	WLCSP90
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B1 Series				Q										,															
	PH 1.8 MB 2.0 MB	1x 160 MHz	1x 46 GOPS	1 1	7 (2 w RS-485) 4	3	1	2	1x up to 24-bit	1 1		8b		3 1	1	1 4	4 2	4	4 1	1	1		1x, 4 differential inpu	_			71 6	1.7V to 4.5V	FBGA120
AB1C1F4M51820F	HH 1.8 MB 2.0 MB	1x 160 MHz	1x 46 GOPS	1	7 (2 w RS-485) 4	3	1	2		1 1		8b	Ĭ	3 1	1	1 4	4 2	4	1 1	1	1	2x, 12 inputs	1x, 4 differential inpu	ts 1x, 1 channel	2x, 8 inputs	1x, 4 inputs	36 6	1.7V to 4.5V	WLCSP90
AB1C1F1M41820P	─ ♦	1x 160 MHz		1	7 (2 w RS-485) 4	3	1					8b	Ĭ	3 1	1	1 4	4 2	4	1 1	1	1	2x, 12 inputs	1x, 4 differential inpu	ts 1x, 1 channel	2x, 8 inputs	1x, 4 inputs	71 6	1.7V to 4.5V	FBGA120
AB1C1F1M41820H	_	1x 160 MHz		1	7 (2 w RS-485) 4	3	1					8b	Ĭ	3 1	1	1 4	4 2	4	1 1	1	1	2x, 12 inputs	1x, 4 differential inpu	ts 1x, 1 channel	2x, 8 inputs	1x, 4 inputs	36 6	1.7V to 4.5V	WLCSP90
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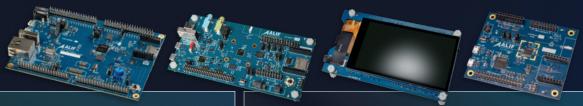












Development Kits

Alif DevKits enable rapid evaluation of hardware features by bringing out as many signals as possible to headers for prototyping, performance evaluation and easy test and measurement access. Several pre-built applications are available for all DevKit platforms to help you quickly get started and are available in the following kit selections:

The Ensemble DevKit (DK-E7) features the superset E7 series device having two Cortex-M55 CPU cores, two Ethos-U55 neural network processors cores, and two Cortex-A32 MPU cores. The E7 MCU in this kit can be configured to operate like other Ensemble MCUs having less cores, allowing you to explore the E5, E3, and E1 series devices of the Ensemble family using just one kit.

The Ensemble EIC DevKit (DK-EIC) lets you explore the Compact series of Ensemble devices. Despite its small physical size, EIC packs quite a punch with its Cortex-M55 CPU core, Arm Ethos-U55 micro NPU, and the lowest power consumption across the Ensemble family.

The Balletto DevKit (DK-BI) introduces the Balletto B1 series, the world's first wireless MCU with integrated hardware acceleration for AI/ML workloads. Balletto combines Bluetooth Low Energy 5.3 and 802.15.4 based Thread protocols, an Ethos-U55 microNPU for AI acceleration, and a powerful Cortex-M55 MCU core.

Application Kits

Alif AppKits are application-oriented platforms for rapid software prototyping. They are supported by a software SDK that contains support for all board level peripherals, enabling quick prototyping of diverse use-cases.

The Ensemble AIML AppKit (AK-E7-AIML) enables rapid software prototyping and evaluation of endpoint machine learning use cases. Powered by the quad core Alif Ensemble E7 fusion processor, this kit is ideal for experiencing the performance uplift Alif delivers for Endpoint ML projects over traditional MCUs. On board is a MIPI-CSI camera/image sensor for snapshots or video input, four microphones, and an IMU sensor to capture motion.

Start Kits

Alif StartKits enable a very low cost way to enter the Ensemble and Balletto families with these small but extremely useful kits.

The Ensemble StartKit (SK-EIC) and The Balletto StartKit (SK-BI) provide access to pins for easy prototyping of your projects. Two microphones are on board and a camera module may be attached. SK-Bl includes the BLE RF path including the antenna.



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Schedule a meeting with our team to ensure a seamless integration to Alif devices: contact@alifsemi.com